

晶圆测试 3D AOI
引线键合测试 3D AOI

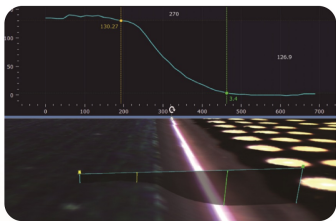
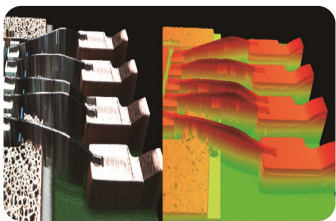
ZEUS *The Best 3D Technology*



ZEUS

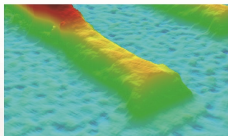
Wafer Bump & Wire Bonding 3D AOI

技术特性

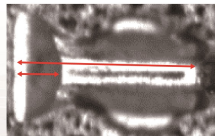
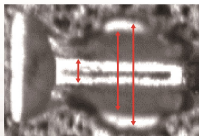


ZEUS是基于PEMTRON公司创新的3D视觉技术，以超过15年的3D经验而研发，这足以使我们能够引领半导体行业。

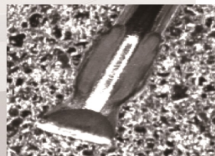
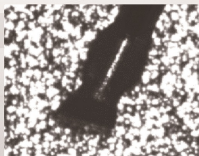
我们所拥有的经验和知识，以及对这些技术的理解 and 应用，使我们能够专注于半导体产品测试，在这个行业中，更能显示出我们设备的品质、性能和技术。



清晰的3D图像



引线检测

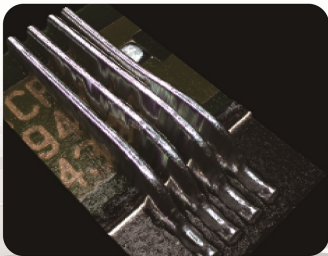
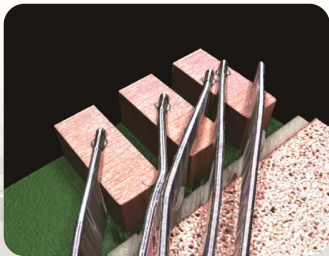
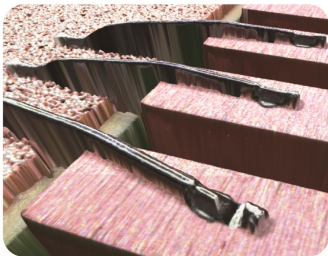
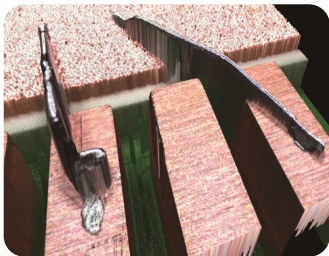


2D图像

最佳引线键合测试 3D AOI

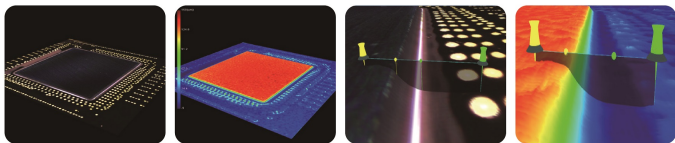
检测能力

- 金线缺失
- 金线损坏
- 断线、塌线
- 金线浮起
- 金线损伤
- 金线短路
- 不键合
- 弧高不良
- 切割线、金线断开
- 键合点偏移



最佳的3D检测系统

ZEUS使用了Moiré条纹技术与PEMTRON自主开发相结合的先进的光学检测技术，是经过优化的最佳检测系统。



高精度晶圆芯片检测无反射干扰

主要特点

○ 晶圆芯片检测

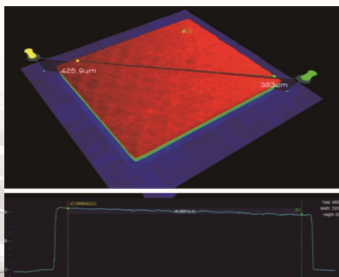
- 反射式晶圆芯片表面检测
- 综合检测结果复查与分析

○ 3D检测能力

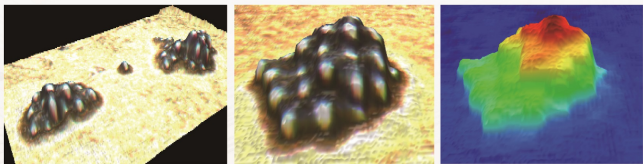
- 清晰的分辨率
- 最优的图像处理与算法

○ 适用场景

SIP/FCBGA/FOWLP/FOPLP/WLCSP



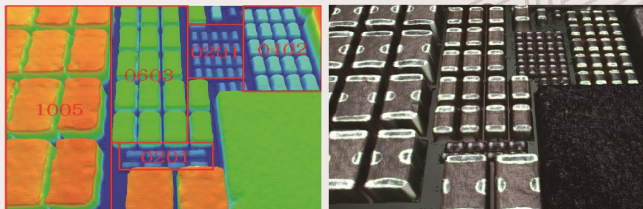
AOI + SPI多检测系统



- ❖ 小型器件 (比008004更小) 检测
- ❖ 完整的检测复判与分析
- ❖ 真正3D检测功能
- ❖ 最优的图像处理与算法
- ❖ 高速GPU与图像处理

小元件检测能力

- ❖ 通过卓越的创新解决您的检测难题



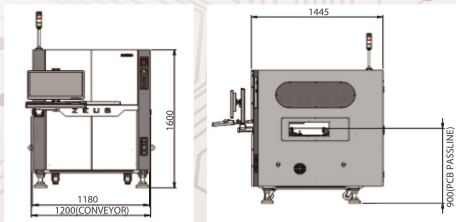
ZEUS (Wafer Bump & Wire Bonding 3D AOI)

SPECIFICATIONS

ZEUS	
Handling Media	Metal Carrier, JEDEC Tray, Wafer Ring Frame, Bare Wafer, Strip on Boat, Laminate
Package Types	SIP, FCBGA, FOWLP / FORPLP, WLCSF
Inspection Types	Bump- Print Bump, Coined Bump, Solder ball Components: Capacitor & Resistor(Body, Solder Fillet, Joint, Body) Silicon Die Wire

Dimensions

1200 × 1445 × 1600 (W×D×H)
200~240AC, 50/60Hz single Phase, 5kgf/cm²



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